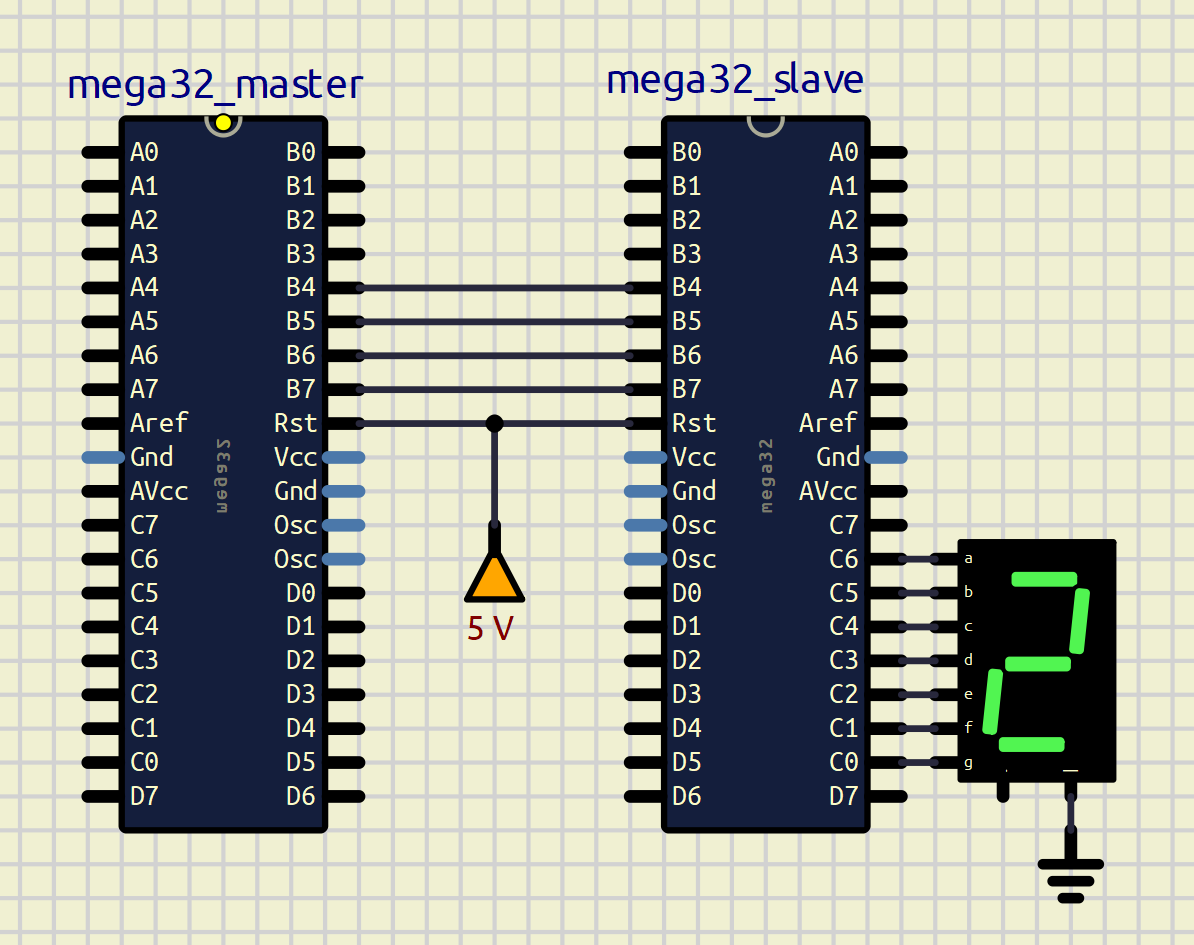
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| Embedded Systems Lab Experiment 10 |
| Serial Peripherial Interface in ATmega 32 |
| To program two ATmega 32 microcontroller as a master and a slave for SPI communication between them to control a 7-segment display |

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| Jitbitan Baroi  3-31-2024 |



Schematic used for SPI Communication with master and slave boards mentioned